

Shashi Kant Sharma

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OBJECTIVE

To pursue summer internship where I can contribute my skills and build my research experience.

AREA OF INTEREST

- Computer & Wireless Networks
- Mobile Computing
- Data Base and Data Streaming
- Compilers & Programming Languages
- Digital Image Processing

ACADEMICS

- **Bachelor of Technology(B.Tech+M.tech)**, Department of Computer Science and Engineering,
Indian Institute of Technology (IIT) Kanpur.
CGPA : 7.0/10.0 (after 4th Semester)
- **Class XII**, Senior Secondary Board of Education Rajasthan
R.K. Joshi Senior Secondary School Dausa
Percentage obtained : 87.69%
- **Class X**, Senior Secondary Board of Education Rajasthan
Adarsh Vidhya MAndir School Dausa
Percentage obtained : 83%

SCHOLASTIC ACHIEVEMENTS

- Secured **All India Rank – 374 in IIT-JEE'05** (among **top 0.2%** out of more than **0.2 million** students in the Joint Entrance Examination (JEE) for admission to the Indian Institutes of Technology.)
- Secured **All India Rank –403 in AIEEE'05** (All India Engineering Entrance Examination)

PROJECTS

- **Compiler for Object-Oriented Pascal** (January-08,till now)
Advisor:-**Prof. S. K. Agarwal**, Dept. of Computer Science &Engineering,IIT Kanpur
Designing a compiler for object-oriented pascal . Features are method overloading, arithmetic and Boolean operations.
- **Web Proxy Server** (September, 2007)
Advisor : **Prof. Dheeraj Sanghi**, Dept. of Computer Science & Engineering, IIT Kanpur.
Designed a Web proxy server that is capable of delivering Web content on behalf of a remote Web server. The server accepted multiple client requests.
- **Extending Nachos** (August - September, 2007)
Advisor : **Prof. Rajat Moona**, Dept. of Computer Science & Engineering, IIT Kanpur.

This project involved adding system calls, implementing multitasking, scheduling of threads, implementing virtual memory, paging, handling page faults. All these additions were done to the basic framework of the operating system Nachos.

- **Algorithm Coding** (June - July, 2007)
Advisor : **Prof. Phalguni Gupta**, Dept. of Computer Science & Engineering, IIT Kanpur.
As a Summer project I worked on all kind of algorithms by exploring the problems on ACM website and coded them using C and Java.
- **Implementing SDLX processor on FPGA board** (February – March,2007)
Advisor : **Prof. Ajay Jain**, Dept. of Computer Science & Engineering, IIT Kanpur.
It comprised of designing SDLX processor on Spartan-3 Starter Kit Board by designing ALU, designing register file, and then implementing instructions of SDLX processor using Verilog HDL.

TECHNICAL SKILLS

- **Programming Languages** : C, C++, Java, HTML, Assembly language for Intel IA32 Architecture
- **Scripting Languages** : Shell Scripting, Perl, PHP, Latex, JAVA script
- **Platforms**: Windows NT/XP/Vista, Linux
- **Hardware** : Verilog, Spartan families of Xilinx platform FPGAs
- **Other Tools** : Lex, Yacc, Awk, Matlab

RELEVANT COURSES

- **Computer Science:** Data Structures and Algorithms
Computer Organisation
Discrete Mathematics
Computer Networks
Theory of Computation
Operating Systems
Principles of Database Systems
Compiler Design
- **Mathematics :** Real Analysis
Linear Algebra and Complex Numbers
Differential Equation and Calculus
- **Other Courses:** Digital Image Processing
Computational Methods in Engineering
Engineering Graphics

EXTRA CURRICULAR ACTIVITIES

- Playing outdoor games such as Cricket, Volleyball, Badminton
- Secretary, Control Room, Antaragni 2006- Cultural Festival of IIT Kanpur.